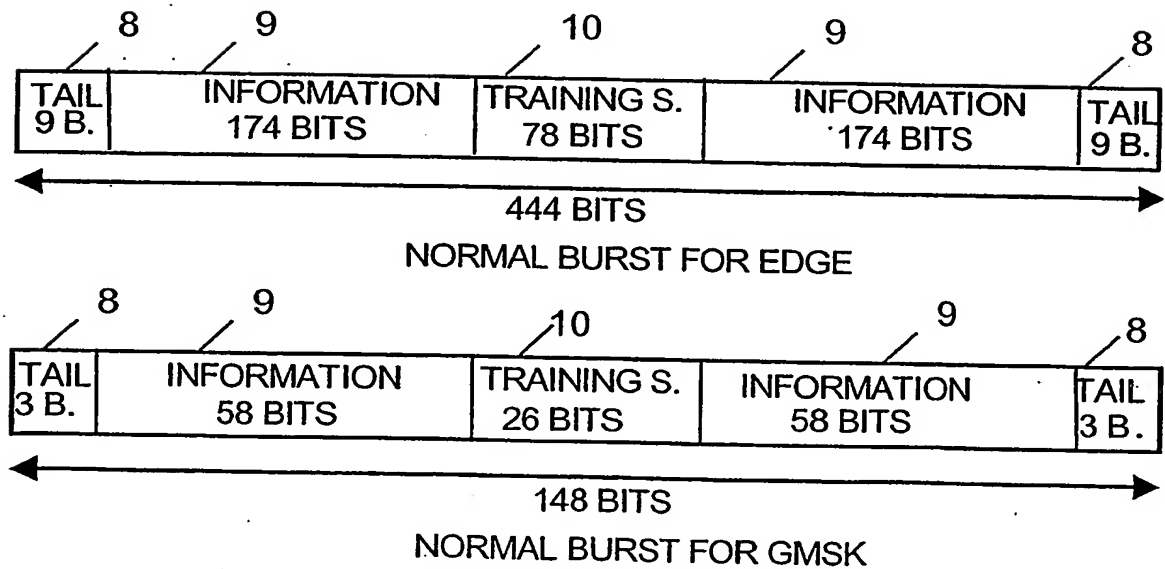
**Fig. 1****Fig. 2**

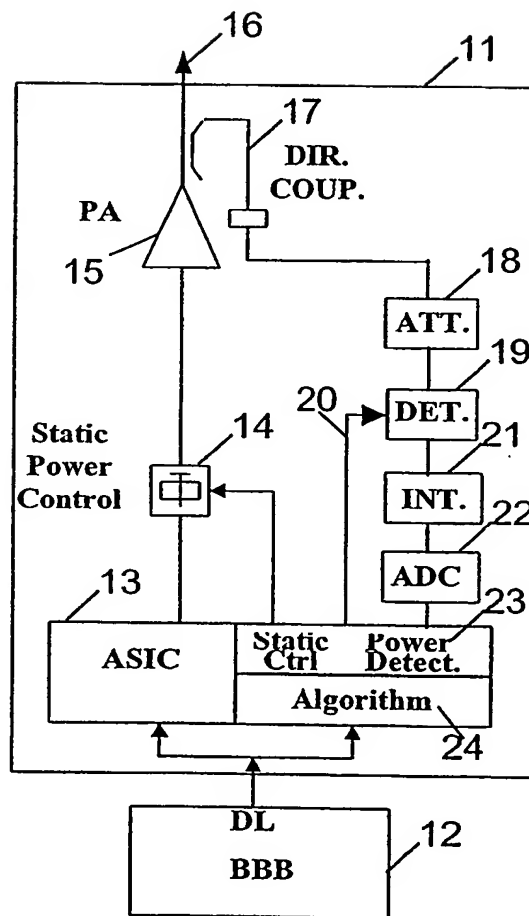


Fig. 3

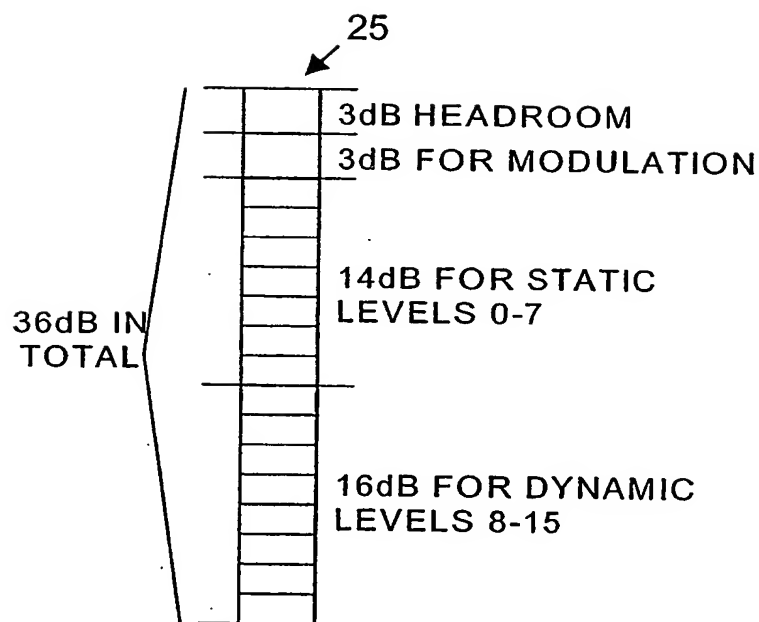


FIG. 4

10/506 322

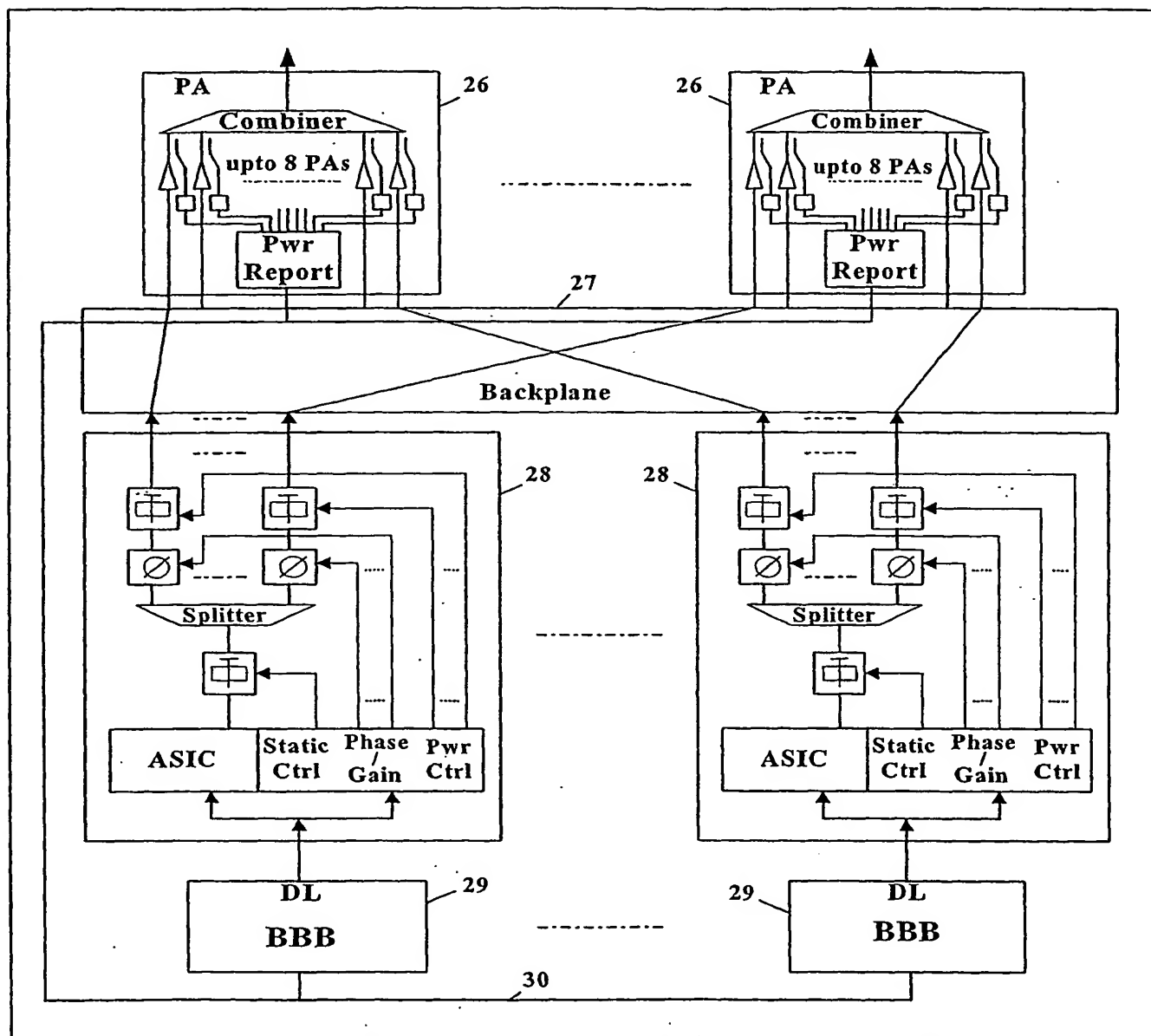


Fig. 5

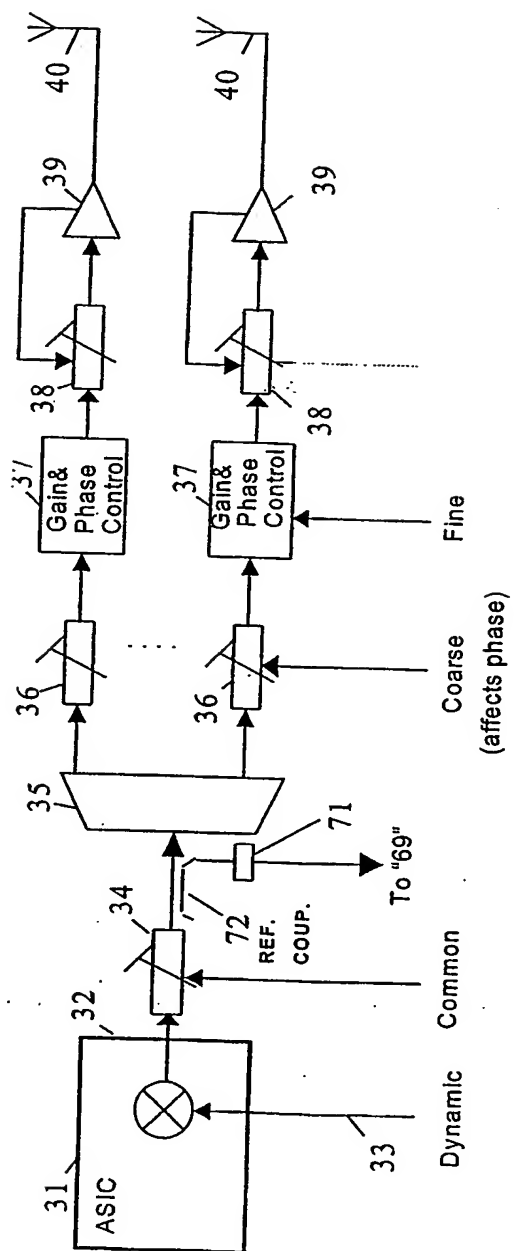


Fig. 6

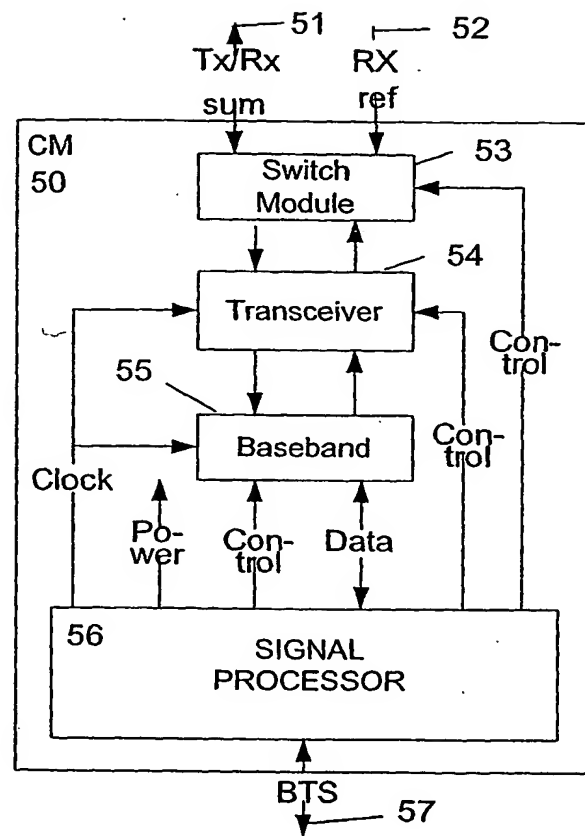
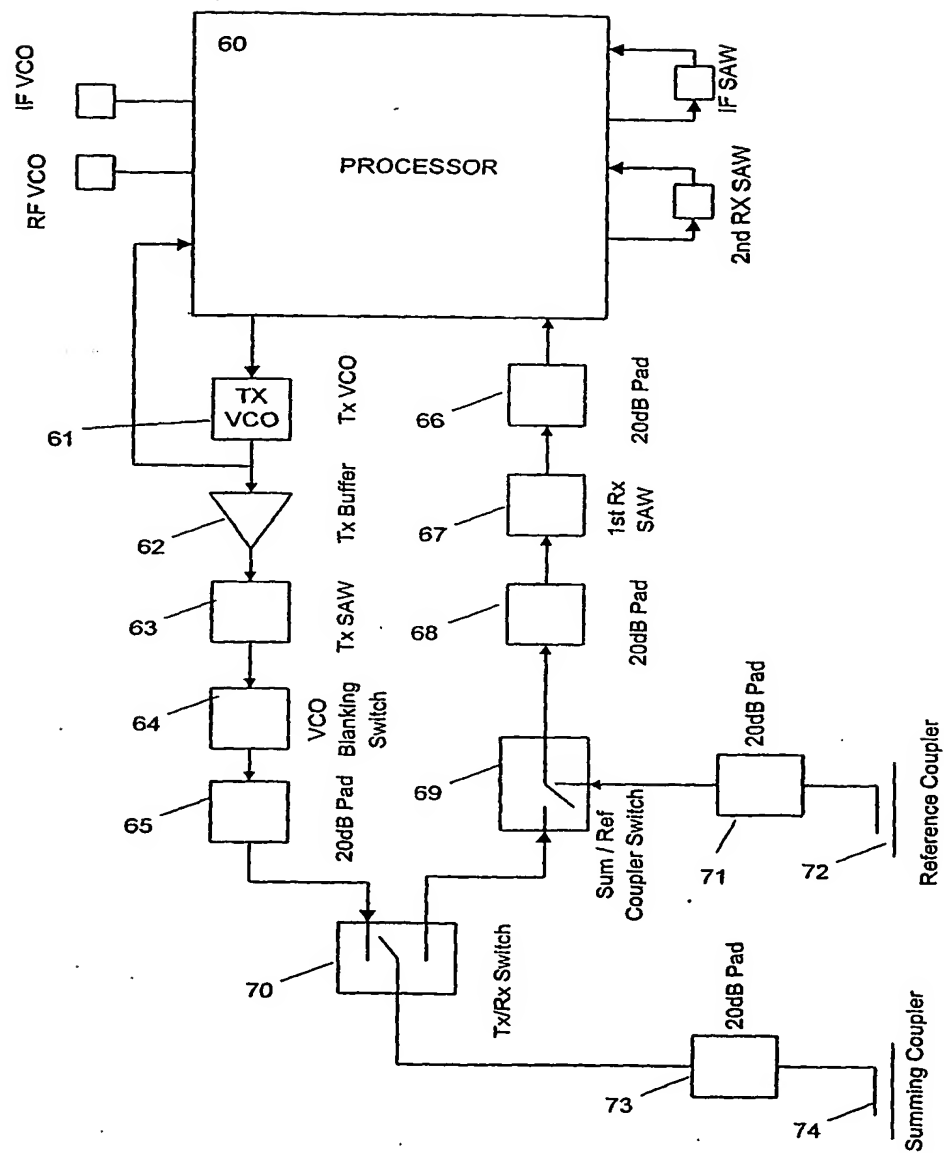
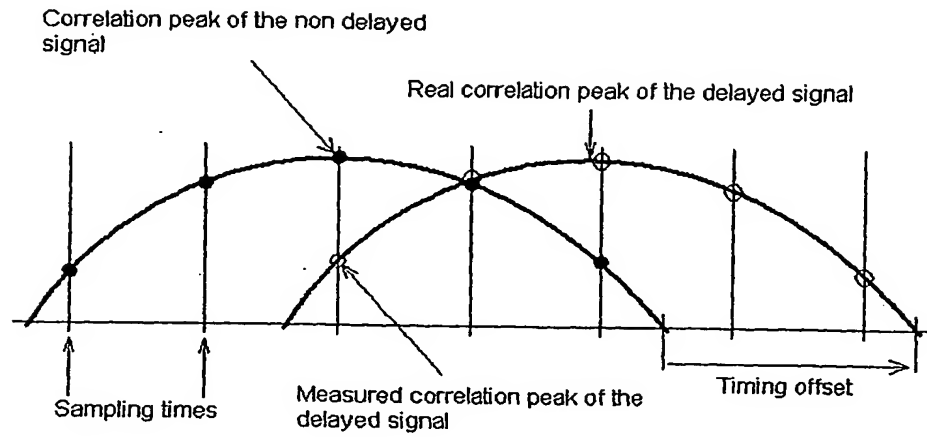
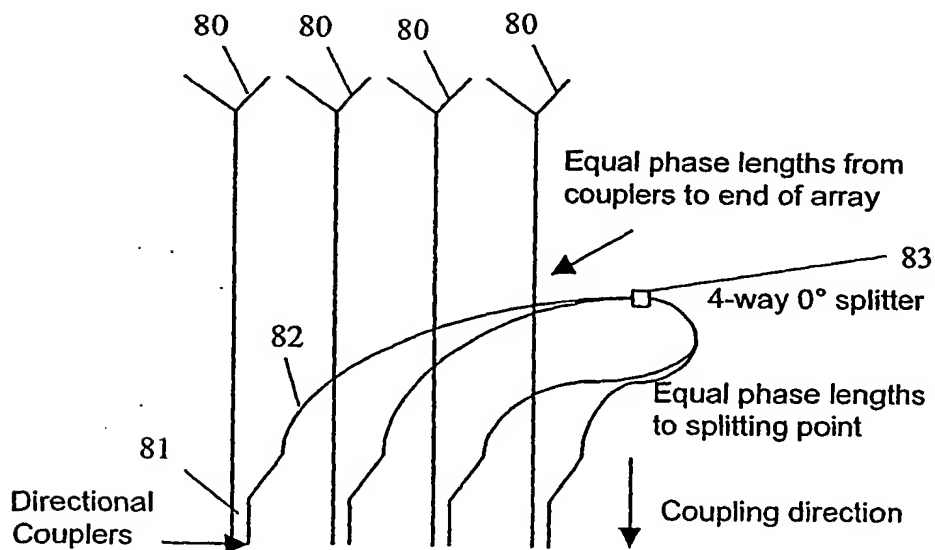


FIG. 7

**FIG. 8****RF BLOCK ARCHITECTURE**

**FIG. 9****CORRELATION PEAK WITH AND WITHOUT TIMING OFFSET****FIG. 10**